Faculty of Applied Science  
School of Engineering  
POSITIONS – LAB TEACHING ASSISTANT, TEACHING ASSISTANT MARKER  
(UTA or GTA)

The School of Engineering is inviting applicants to apply for an Undergrad and/or Graduate Teaching Assistant position for the following course:

**APSC 262 – Digital Logic Design**  
Logic design methods, hardware description language (HDL), number representation and arithmetic circuits, combinational circuits, flip-flops, registers, programmable logic devices (FPGAs), counters, finite state machines, digital system designs.

**Start Date:** January 9, 2023  
**End Date:** April 28, 2023

**Description of Duties:** The successful candidate will conduct lab sections or marking under the supervision of the course instructor.

**The estimated number of hours per lab section:** 2 hours. This hourly position will not exceed 12 hours per week.

**Number of positions available:** 1-3 positions

**Wage for this position:** As per the BCGEU Collective Agreement UTA or GTA.

**Eligibility:** To be eligible to apply, applicants must be enrolled in a full-time undergraduate or graduate degree program at UBC Okanagan. Successful Candidates are required to be in Canada for the duration of the position. (Note: a full-time student is defined as having a minimum of 24 credits for the 2022 winter session).

**TO APPLY FOR THIS POSITION:** Please complete online TA application form  
**APPLICATION DEADLINE:** April 30, 2022

*Equity and diversity are essential to academic excellence. An open and diverse community fosters the inclusion of voices that have been underrepresented or discouraged. We encourage applications from members of groups that have been marginalized on any grounds enumerated under the B.C. Human Rights Code, including sex, sexual orientation, gender identity or expression, racialization, disability, political Policy #HR117 belief, religion, marital or family status, age, and/or status as a First Nation, Metis, Inuit, or Indigenous person.*