Faculty of Applied Science
School of Engineering
POSITIONS – LAB TEACHING ASSISTANT, TEACHING ASSISTANT MARKER
(UTA or GTA)

The School of Engineering is inviting applicants to apply for an Undergrad and/or Graduate Teaching Assistant position for the following course:

ENGR 466 – Introduction to VLSI Systems
The chip design process using VLSI design styles in CMOS technology. Data path, control and register file design and layout. Clocking schemes, flip-flop and latch-based design. Design project using CAD tools.

Start Date: January 9, 2023
End Date: April 28, 2023

Description of Duties: The successful candidate will conduct lab sections or marking under the supervision of the course instructor.

The estimated number of hours per lab section: 2 hours. This hourly position will not exceed 12 hours per week.

Number of positions available: 1-2 positions

Wage for this position: As per the BCGEU Collective Agreement UTA or GTA.

Eligibility: To be eligible to apply, applicants must be enrolled in a full-time undergraduate or graduate degree program at UBC Okanagan. Successful Candidates are required to be in Canada for the duration of the position. (Note: a full-time student is defined as having a minimum of 24 credits for the 2022 winter session).

TO APPLY FOR THIS POSITION: Please complete online TA application form

APPLICATION DEADLINE: April 30, 2022

Equity and diversity are essential to academic excellence. An open and diverse community fosters the inclusion of voices that have been underrepresented or discouraged. We encourage applications from members of groups that have been marginalized on any grounds enumerated under the B.C. Human Rights Code, including sex, sexual orientation, gender identity or expression, racialization, disability, political Policy #HR11 7 belief, religion, marital or family status, age, and/or status as a First Nation, Metis, Inuit, or Indigenous person.